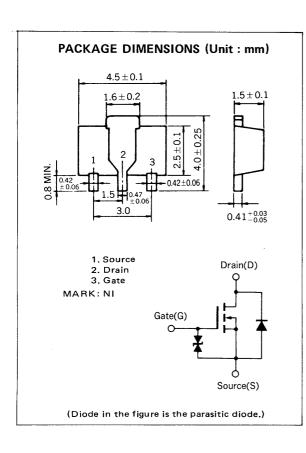
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# MOS FIELD EFFECT TRANSISTOR 2SK1586

# N-CHANNEL MOS FET FOR SWITCHING



The 2SK1586, N-channel vertical type MOS FET, is a switching device which can be driven directly by the output of ICs having a 5 V power source.

As the MOS FET has low on-state resistance and excellent switching characteristics, it is suitable for driving actuators such as motors, relays, and solenoids.

#### **FEATURES**

- Directly driven by ICs having a 5 V power source.
- Has low on-state resistance.

$$\begin{split} R_{DS(on)1} &= 1.0~\Omega~MAX. @~V_{GS} = 4.0~V, ~I_D = 0.5~A \\ R_{DS(on)2} &= 0.6~\Omega~MAX. @~V_{GS} = 10~V, ~I_D = 0.5~A \end{split}$$

#### **QUALITY GRADE**

Standard

Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

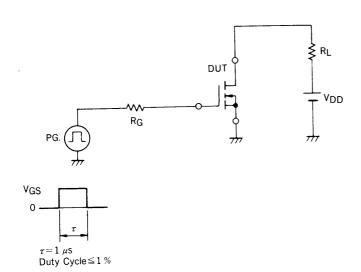
# ABSOLUTE MAXIMUM RATINGS ( $T_a = 25$ °C)

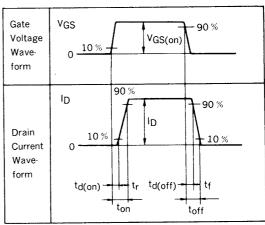
PARAMETER	SYMBOL	RATINGS	UNIT	TEST CONDITIONS
Drain to Source Voltage	V <sub>DSS</sub>	30	V	V <sub>GS</sub> = 0
Gate to Source Voltage	V <sub>GSS</sub>	±20	V	V <sub>DS</sub> = 0
Drain Current	ID(DC)	±1.0	Α	
Drain Current	I <sub>D</sub> (pulse)	±2.0	А	PW ≤ 10 ms, Duty Cycle ≤ 50 %
Total Power Dissipation	P <sub>T</sub>	2.0	w	When using ceramic board of 16 cm <sup>2</sup> x 0.7 mm
Channel Temperature	T <sub>ch</sub>	150	°C	
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C	

# ELECTRICAL CHARACTERISTICS ( $T_a = 25$ °C)

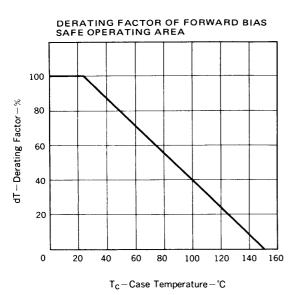
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain Cut-off Current	IDSS	<del></del>		1.0	μА	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0
Gate Leakage Current	IGSS			±10	μА	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0
Gate Cut-off Voltage	VGS(off)	1.3	1.9	2.5	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Forward Transfer Admittance	lyfsl	0.4			S	V <sub>DS</sub> = 5.0 V, I <sub>D</sub> = 0.5 A
Drain to Source On-State Resistance	RDS(on)1		0.3	1.0	Ω	V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 0.5 A
Drain to Source On-State Resistance	R <sub>D</sub> S(on)2		0.2	0.6	Ω	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 A
Input Capacitance	C <sub>iss</sub>		170		pF	V <sub>DS</sub> = 5.0 V, V <sub>GS</sub> = 0, f = 1 MHz
Output Capacitance	Coss	-	170		pF	
Feedback Capacitance	C <sub>rss</sub>		55		pF	
Turn-On Delay Time	<sup>t</sup> d(on)		50		ns	
Rise Time	t <sub>r</sub>		220		ns	$V_{GS(on)} = 5.0 \text{ V, R}_{G} = 10 \Omega$ $V_{DD} = 20 \text{ V, I}_{D} = 0.5 \text{ A}$ $R_{L} = 40 \Omega$
Turn-Off Delay Time	t <sub>d</sub> (off)		210		ns	
Fall Time	tf		230		ns	

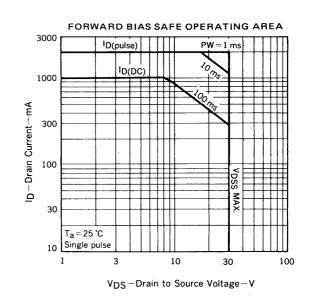
# SWITCHING TIME MEASUREMENT CIRCUIT AND CONDITIONS

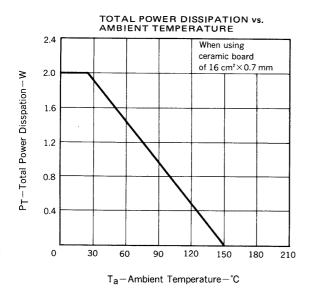


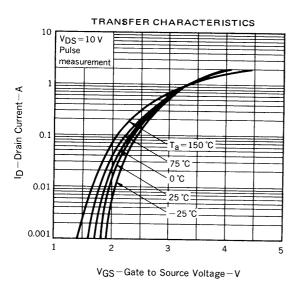


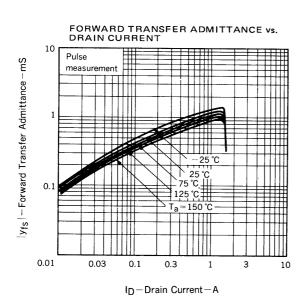
# TYPICAL CHARACTERISTICS (Ta = 25 °C)

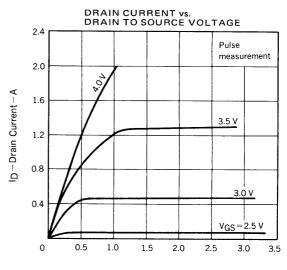


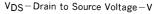


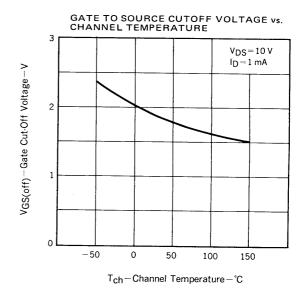


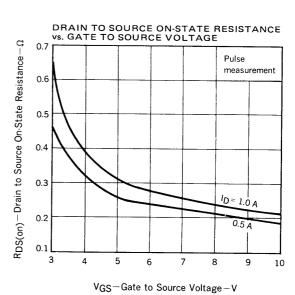


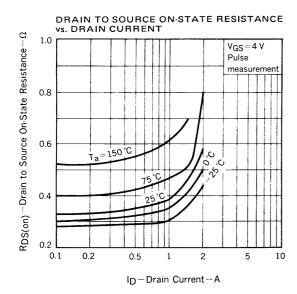


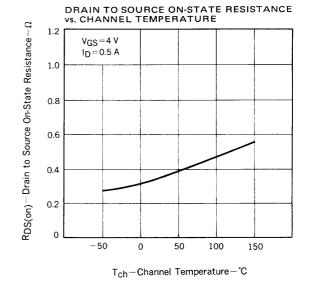


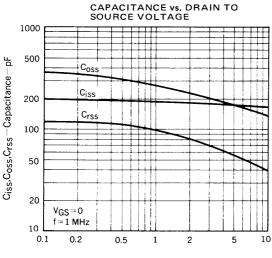


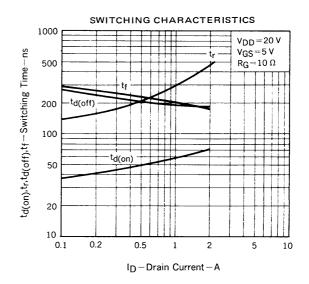




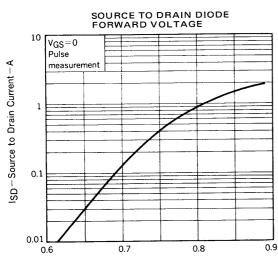








 $v_{DS}\!-\!\text{Drain to Source Voltage-} v$ 



V<sub>SD</sub>-Source to Drain Voltage-V

## RECOMMENDED SOLDERING CONDITIONS

Mounting of this product by soldering should be done under the following conditions.

Please consult our representatives about soldering methods and conditions other than these.

### SURFACE MOUNT TYPE

For details of the recommended soldering conditions, see the information document "SMT MANUAL" (IEI-1207).

Soldering Method	Soldering Conditions	Symbol for Recommended Conditions	
Infrared Reflow	Package peak temp.: 230 °C Soldering time: within 30 sec (above 210 °C) Soldering times: 1, Days limitation: none*	IR30-00	
Vapor Phase Soldering	Package peak temp.: 215 °C Soldering time: within 40 sec (above 200 °C) Soldering times: 1, Days limitation: none*	VP15-00	
Wave Soldering	Soldering bath temp.: below 260 °C Soldering time: within 10 sec Soldering times: 1, Days limitation: none*	WS60-00	

<sup>\*:</sup> Stored days under storage conditions at 25 °C and below 65 % R.H. after the dry-pack has been opened.

Note 1 Combination of soldering methods should be avoided.

#### REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134

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Special: Automotive and Transportation equipment, Traffic control systems, Antidisaster systems, Anticrime systems, etc.

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